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PLUS Search Results for S/N 09977089, Searched January 06, 2005

The Patent Linguistics Utility System (PLUS) is a USPTO automated search system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that are

most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

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6081867 81
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09977089_CLSTITLES

Titles of Most Frequently Occurring Classifications of Patents Returned

From A Search of 09977089 on January 06, 2005

8	710/260	(6 OR, 2 XR)
	Class	710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
	710/260	INTERRUPT PROCESSING
8	710/264	(2 OR, 6 XR)
	Class	710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
	710/260	INTERRUPT PROCESSING
	710/264	.Interrupt prioritizing
7	710/262	(2 OR, 5 XR)
	Class	710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
	710/260	INTERRUPT PROCESSING
	710/262	.Interrupt inhibiting or masking
6	710/269	(4 OR, 2 XR)
	Class	710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
	710/260	INTERRUPT PROCESSING
	710/269	.Handling vector
5	710/263	(0 OR, 5 XR)
	Class	710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
	710/260	INTERRUPT PROCESSING
	710/263	.Interrupt queuing
4	710/266	(3 OR, 1 XR)
	Class	710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
	710/260	INTERRUPT PROCESSING
	710/266	.Programmable interrupt processing
4	711/103	(1 OR, 3 XR)
	Class	711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
	711/100	STORAGE ACCESSING AND CONTROL
	711/101	.Specific memory composition
	711/102	..Solid-state read only memory (ROM)
	711/103	...Programmable read only memory (PROM, EEPROM)

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etc.)

- 3 710/261 (0 OR, 3 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/260 INTERRUPT PROCESSING
710/261 .Multimode interrupt processing
- 3 710/265 (2 OR, 1 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/260 INTERRUPT PROCESSING
710/264 .Interrupt prioritizing
710/265 ..Variable
- 3 710/267 (0 OR, 3 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/260 INTERRUPT PROCESSING
710/267 .Processor status
- 3 710/268 (1 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT
710/260 INTERRUPT PROCESSING
710/268 .Source or destination identifier
- 3 711/152 (2 OR, 1 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/147 .Shared memory area
711/152 ..Memory access blocking
- 3 711/158 (2 OR, 1 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/154 .Control technique
711/158 ..Prioritizing
- 3 713/322 (3 OR, 0 XR)
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: SUPPORT
713/300 COMPUTER POWER CONTROL
713/320 .Power conservation
713/322 ..By clock speed control (e.g., clock on/off)

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2 365/218 (0 OR, 2 XR)
 Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 365/189.01 READ/WRITE CIRCUIT
 365/218 .Erase

2 710/48 (1 OR, 1 XR)
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
 PROCESSING SYSTEMS: INPUT/OUTPUT
 710/1 INPUT/OUTPUT DATA PROCESSING
 710/36 .Input/Output access regulation
 710/48 ..Input/Output interrupting

2 710/49 (0 OR, 2 XR)
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
 PROCESSING SYSTEMS: INPUT/OUTPUT
 710/1 INPUT/OUTPUT DATA PROCESSING
 710/36 .Input/Output access regulation
 710/48 ..Input/Output interrupting
 710/49 ...Masking

2 711/167 (0 OR, 2 XR)
 Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: MEMORY
 711/100 STORAGE ACCESSING AND CONTROL
 711/167 .Access timing

2 712/245 (0 OR, 2 XR)
 Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: PROCESSING ARCHITECTURES AND INS

TRUCTION

PROCESSING
 712/220 PROCESSING CONTROL
 712/245 .Processing sequence control (i.e.,
 microsequencing)

2 712/32 (1 OR, 1 XR)
 Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: PROCESSING ARCHITECTURES AND INS

TRUCTION

PROCESSING
 712/1 PROCESSING ARCHITECTURE
 712/32 .Microprocessor or multichip or multimodule
 processor having sequential program control

2 713/323 (2 OR, 0 XR)
 Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING

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                                09977089_CLSTITLES
                                SYSTEMS: SUPPORT
713/300      COMPUTER POWER CONTROL
713/320      .Power conservation
713/323      ..Active/idle mode processing

2  714/25      (1 OR, 1 XR)
    Class 714 : ERROR DETECTION/CORRECTION AND FAULT
                DETECTION/RECOVERY
714/100      DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING

714/1        .Reliability and availability
714/25      ..Fault locating (i.e., diagnosis or testing)

2  714/704      (0 OR, 2 XR)
    Class~ 714 : ERROR DETECTION/CORRECTION AND FAULT
                DETECTION/RECOVERY
714/699      PULSE OR DATA ERROR HANDLING
714/704      .Error count or rate

2  718/102      (0 OR, 2 XR)
    Class 718 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                SYSTEMS: VIRTUAL MACHINE TASK OR PROCESS MAN
AGEMENT OR TASK
                MANAGEMENT/CONTROL
718/100      TASK MANAGEMENT OR CONTROL
718/102      .Process scheduling

2  718/103      (2 OR, 0 XR)
    Class 718 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                SYSTEMS: VIRTUAL MACHINE TASK OR PROCESS MAN
AGEMENT OR TASK
                MANAGEMENT/CONTROL
718/100      TASK MANAGEMENT OR CONTROL
718/102      .Process scheduling
718/103      ..Priority scheduling

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